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7-12-02
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XA-9627
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Naoki YADA et al.

Appln. No.:

Filed: Herewith

For: MICROCOMPUTER, PROGRAMMING METHOD AND ERASING METHOD

* * *

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified
patent application as indicated below.

IN THE CLAIMS:

Please amend Claims 3, 5 and 9 as set forth below.

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- 1 3. (Amended) The microcomputer according to claim 1,
A₁ 2 further including second register means (FKEY) readable and
3 writable by said CPU, and